



# mec

## NIR SENSORS BASED ON PHOTOLITHOGRAPHICALLY PATTERNED PBS QD PHOTODIODES FOR CMOS INTEGRATION

EPIMITHEAS GEORGITZIKIS,

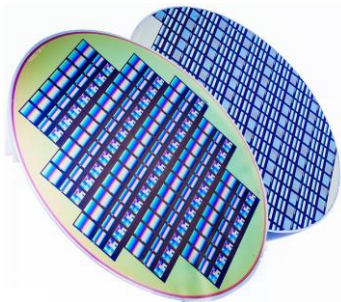
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ANDREAS SÜSS, CELSO CAVACO, KONSTANTINOS CHATZINIS, JORICK MAES, ZEGER HENS, PAUL HEREMANS,  
DAVID CHEYNS



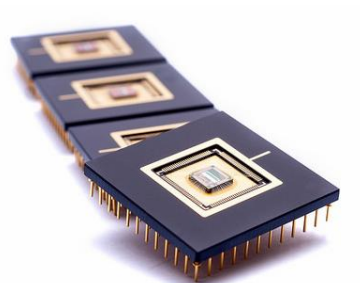
Leuven – R&D HQ  
3500 people

IMEC vzw

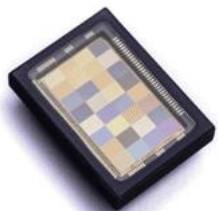
### Image Sensors



design and tech platforms



integration and testing



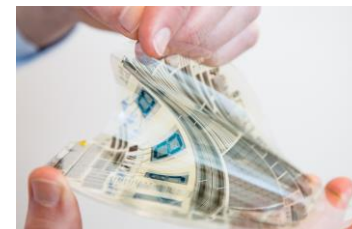
custom image sensors and cameras

*UV, VIS, NIR, hyperspectral, lensfree, integrated photonics*

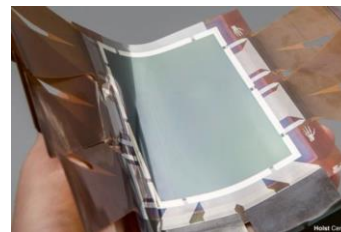
### Thin-Film Electronics



new materials



large area electronics



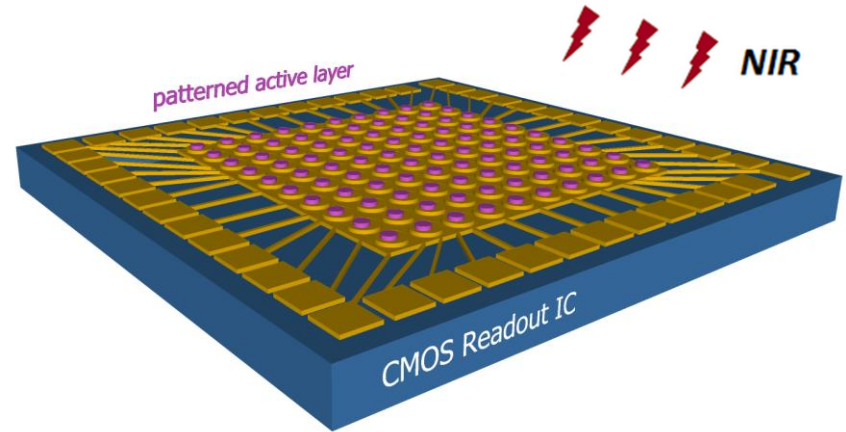
flexible image sensors



thin-film PV

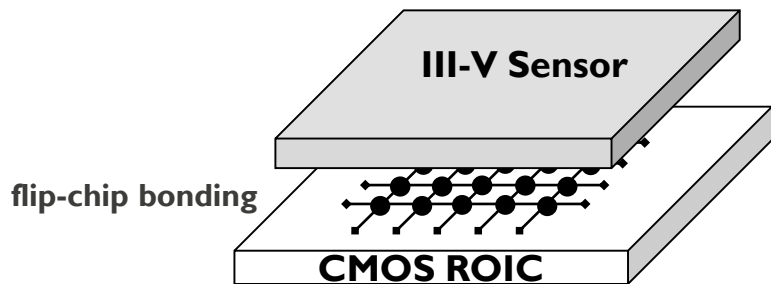
# NIR SENSORS BASED ON PHOTOLITHOGRAPHICALLY PATTERNED PBS QD PHOTODIODES FOR CMOS INTEGRATION

- i. **Our approach: monolithic infrared imager**
- i. **Colloidal quantum dots as an IR absorber**
- ii. **Quantum dot photodiode**
- iii. **CMOS Integration**



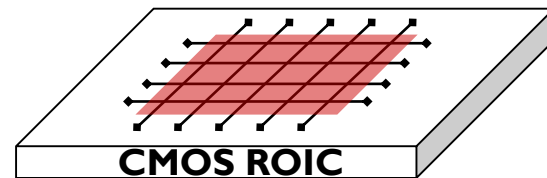
# HYBRID VS MONOLITHIC IMAGER

## HYBRID IMAGER



- + mature technology
- + high QE in III-V materials
- high cost due to hybridization
- low resolution due to flip-chip
- low throughput due to epi substrates

## Monolithic + TFPD

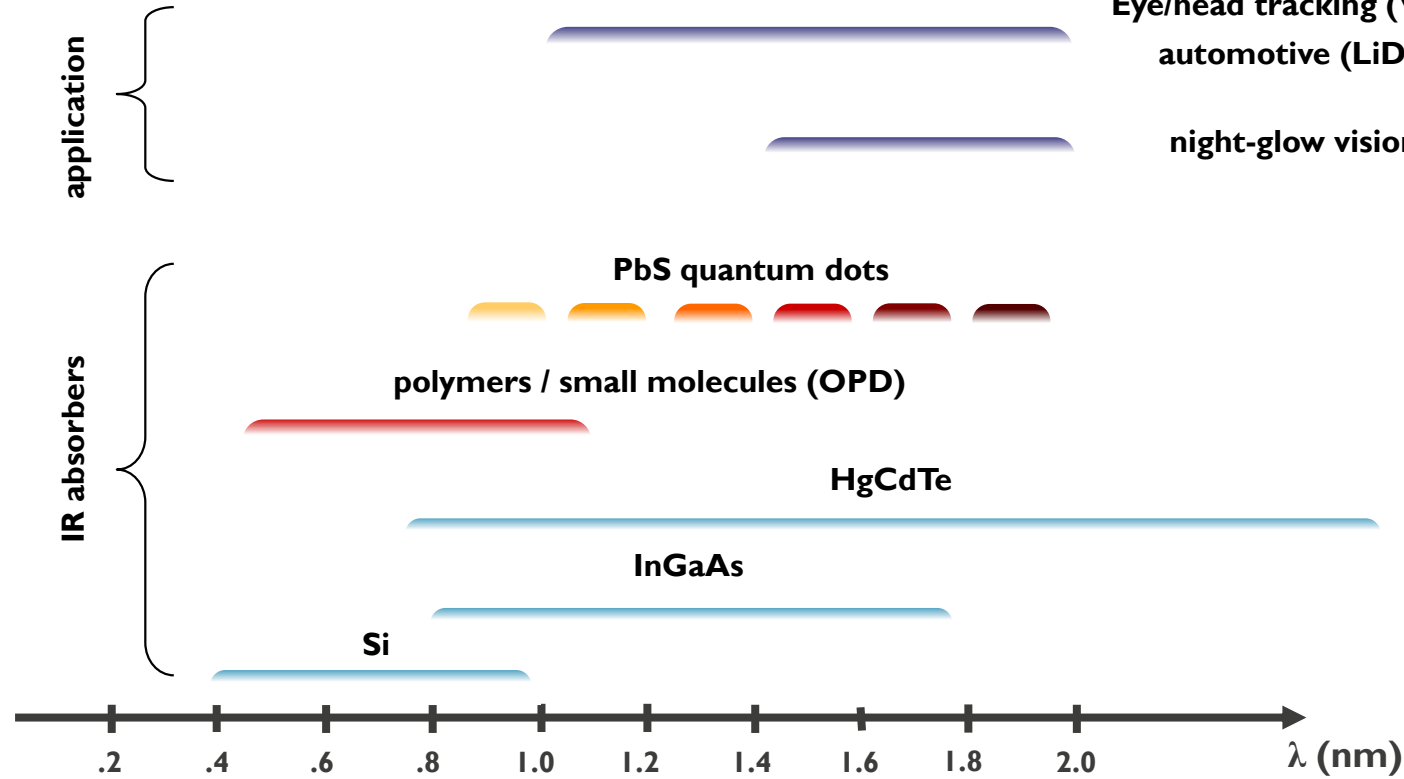


- + small pixel size ( $<5 \mu\text{m}$ )
- + large resolution ( $>1 \text{ MPx}$ )
- + full wafer processing
- + VIS, NIR, VIS+NIR

- ? noise
- ? integration
- ? contamination

# MATERIAL SELECTION

## IR ABSORBERS AND APPLICATIONS





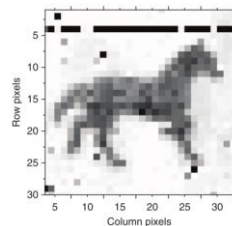
# PREVIOUS DEMONSTRATIONS



**Panasonic**  
VIS with OPC  
0.9  $\mu\text{m}$  CMOS pixel  
2007  $\rightarrow$  now



**Siemens**  
1310 nm with QDPD  
a-Si TFT backplane  
2009



**TU Munchen**  
850 nm with OPD  
15  $\mu\text{m}$  CMOS pixel  
2012



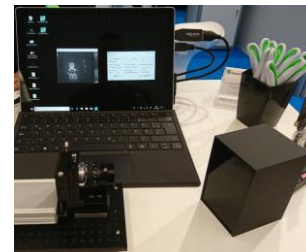
**InVisage / Apple**  
940 nm with Quantum Film  
1.1  $\mu\text{m}$  pixel  
2017

## common challenges:

- processing on full wafers
- integration in fab environment
- noise level with TFPD
- material supply for TFPD
- next-gen: multispectral arrays



**SWIR Vision System**  
400 – 1700nm with TFPD  
15  $\mu\text{m}$  CMOS pixel  
2018



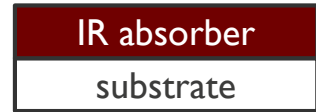
**Fraunhofer FEP**  
940 nm with OPD  
2018

COLLOIDAL QUANTUM DOTS  
AND THE  
QUANTUM DOT PHOTODIODE

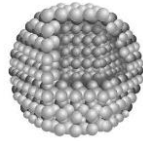


# COLLOIDAL QUANTUM DOTS AS INFRARED ABSORBER

## TUNABLE WAVELENGTH SPECTRUM

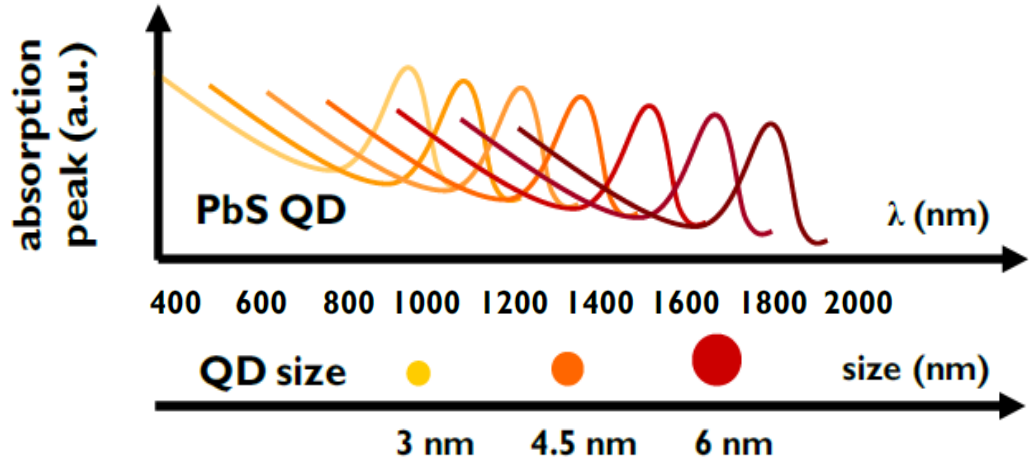


material screening

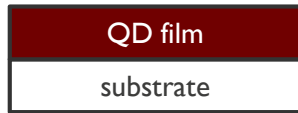
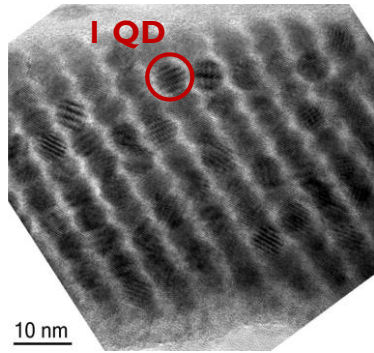


colloidal  
quantum dot

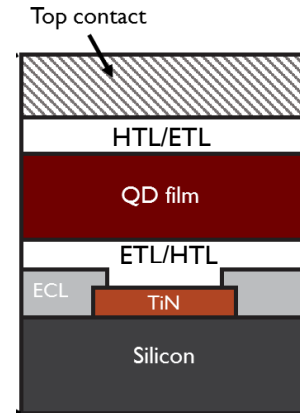
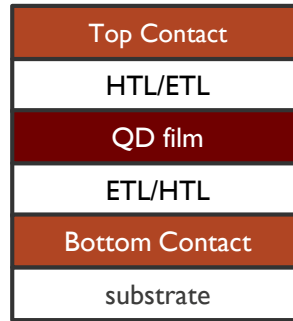
Semiconductors known from displays  
Tunable absorption peak  
Infrared sensitive



# QUANTUM DOT PHOTODIODE



## P-N or N-P photodiode configuration

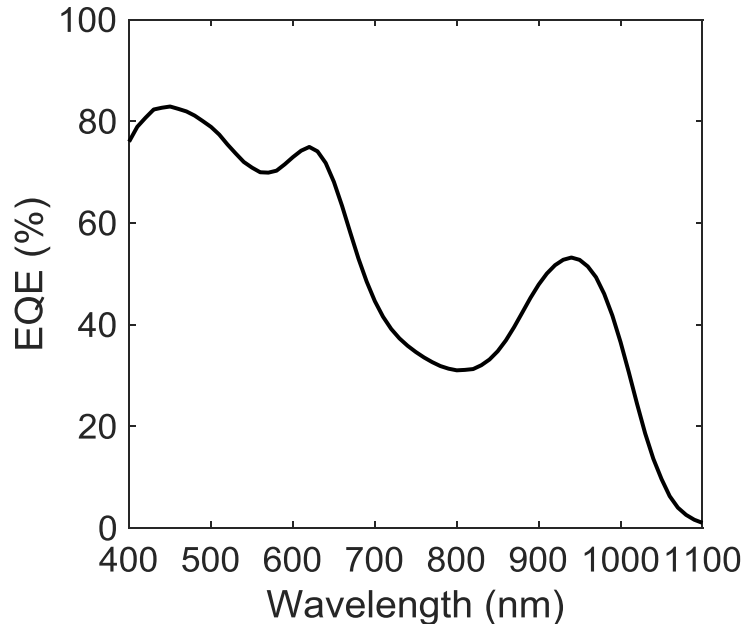


# PHOTODETECTOR STACK DEVELOPMENT

EQE: > 55% in NIR

Dark current at -1V: < 100 nA/cm<sup>2</sup> @300K

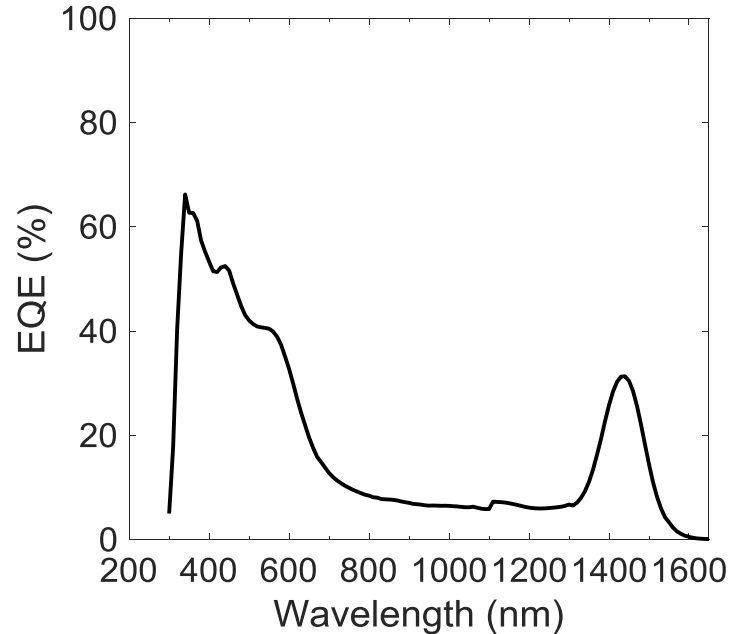
Detectivity (D\*) at 940 nm: 10<sup>12</sup> Jones



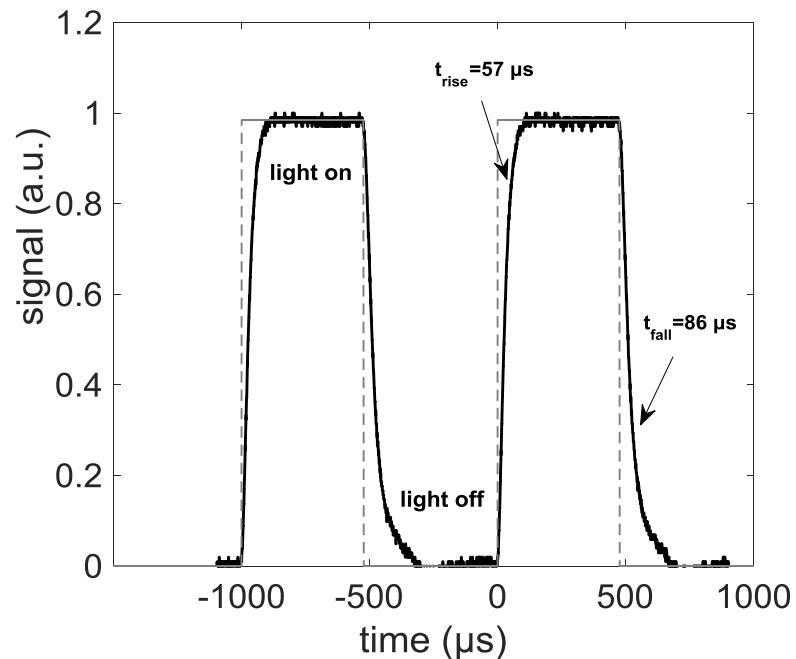
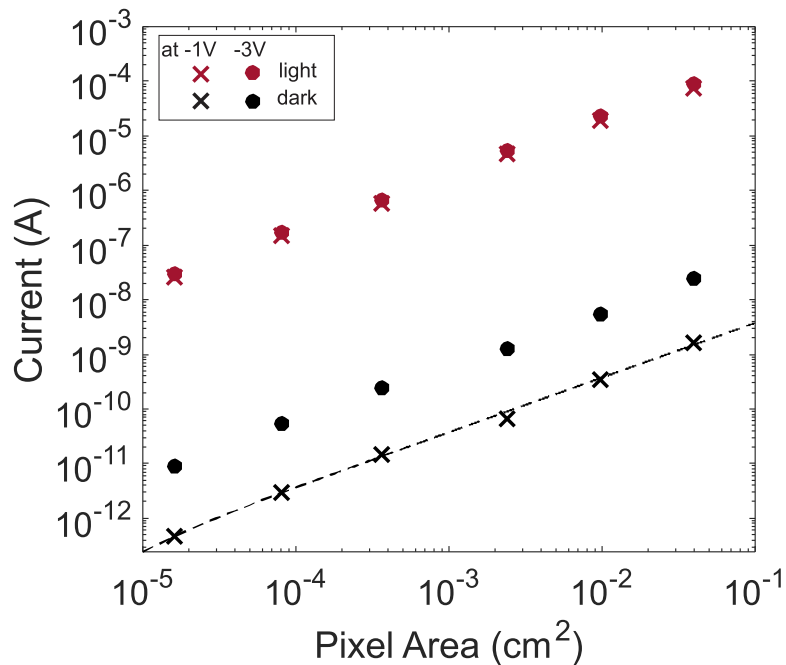
EQE: ~ 30% in SWIR

Dark current at -2V: 1 μA/cm<sup>2</sup> @300K

Detectivity (D\*) at 1450 nm: 5x10<sup>11</sup> Jones

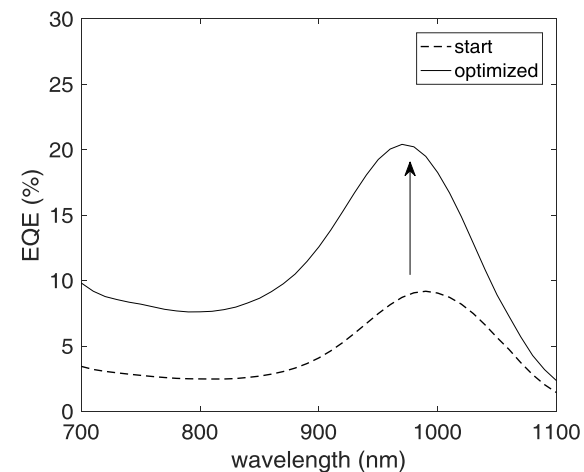
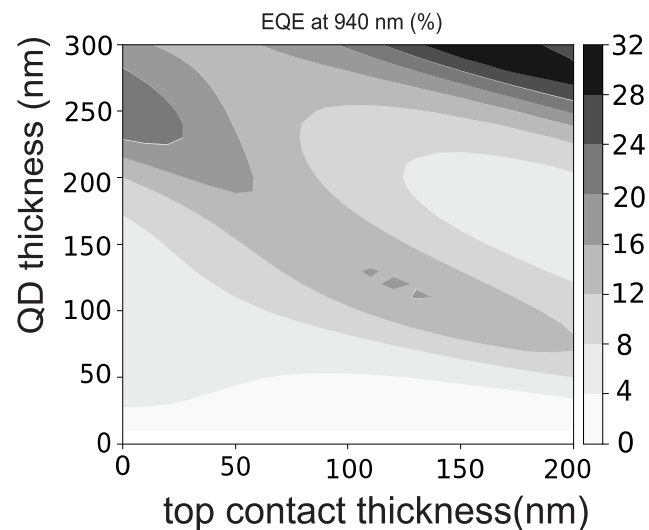
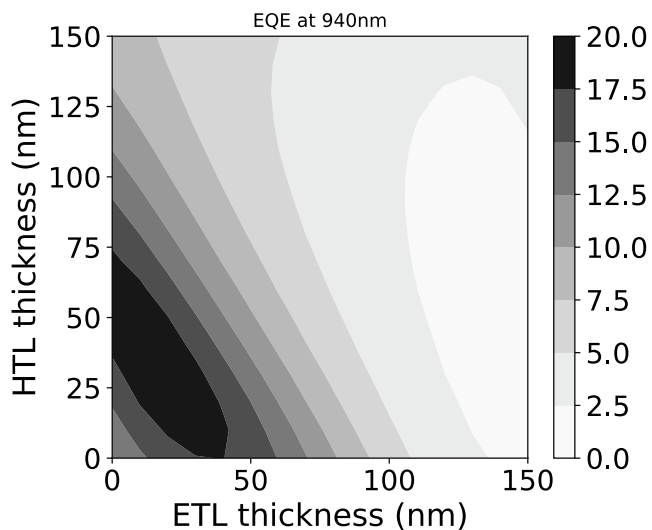


# PHOTODETECTOR STACK DEVELOPMENT



# OPTICAL CAVITIES IMPROVE EQE PERFORMANCE

## TUNING LAYER THICKNESSES WITH OPTICAL MODELING



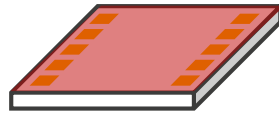
# ROIC INTEGRATION AND UPSCALING



# INTEGRATION ROUTE

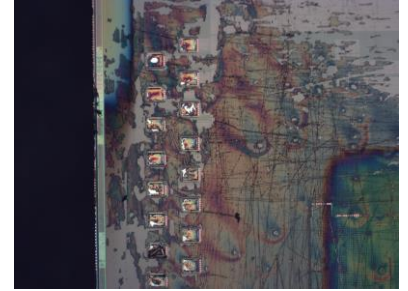


ROIC with  
contact pads

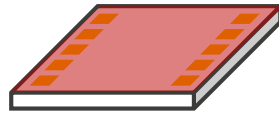


stack deposition  
(spin-coating)

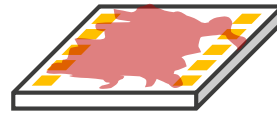
# INTEGRATION ROUTE



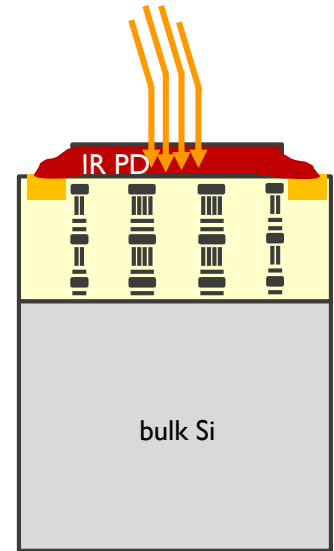
ROIC with  
contact pads



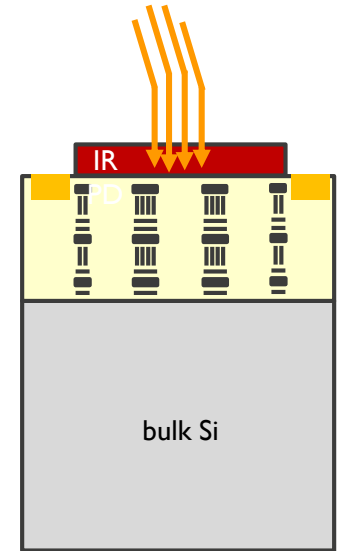
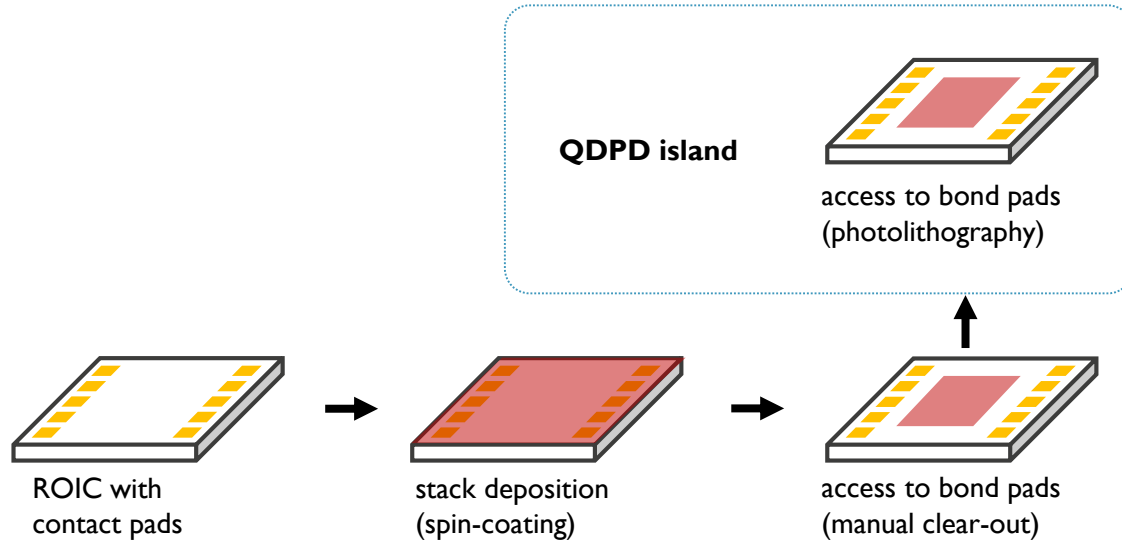
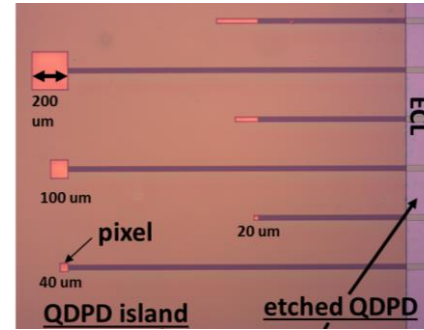
stack deposition  
(spin-coating)



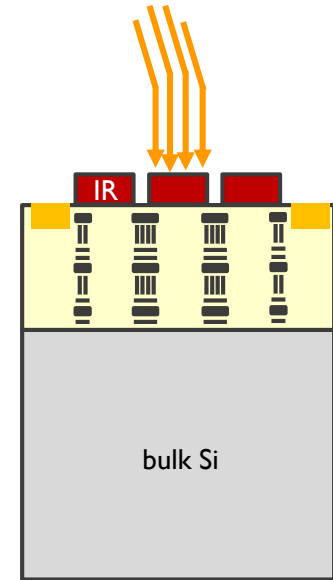
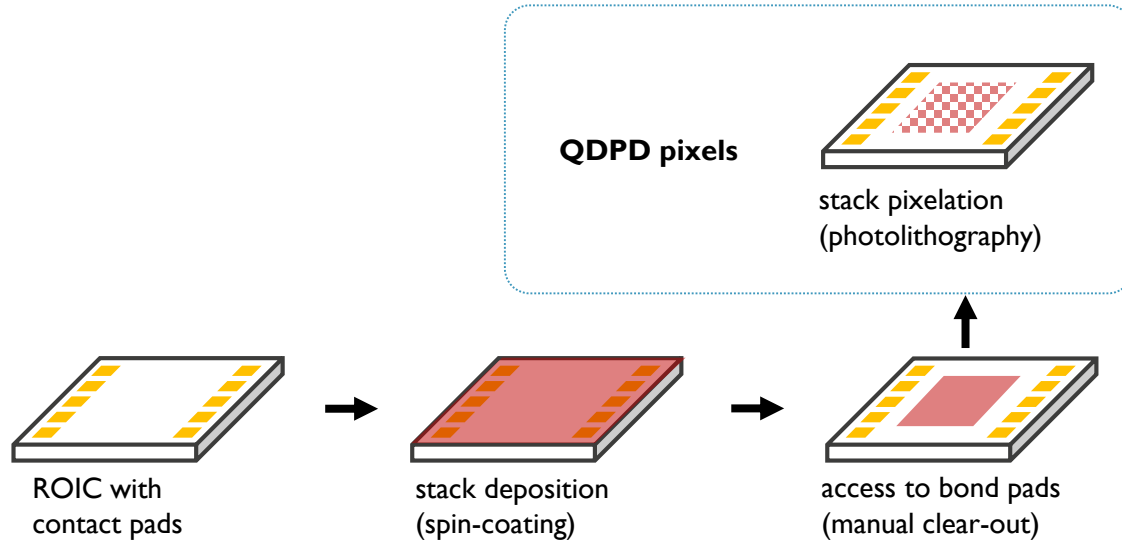
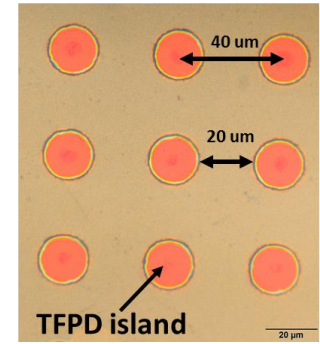
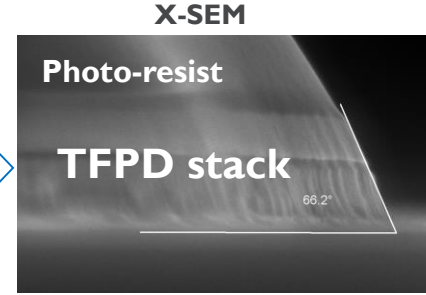
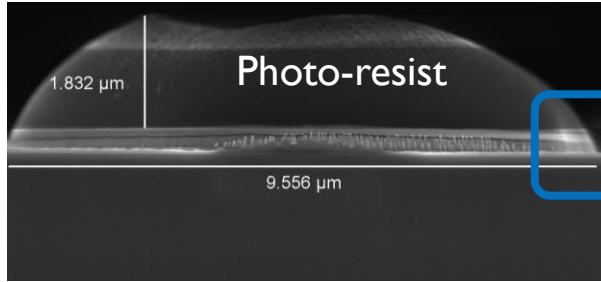
access to bond pads  
(manual clear-out)



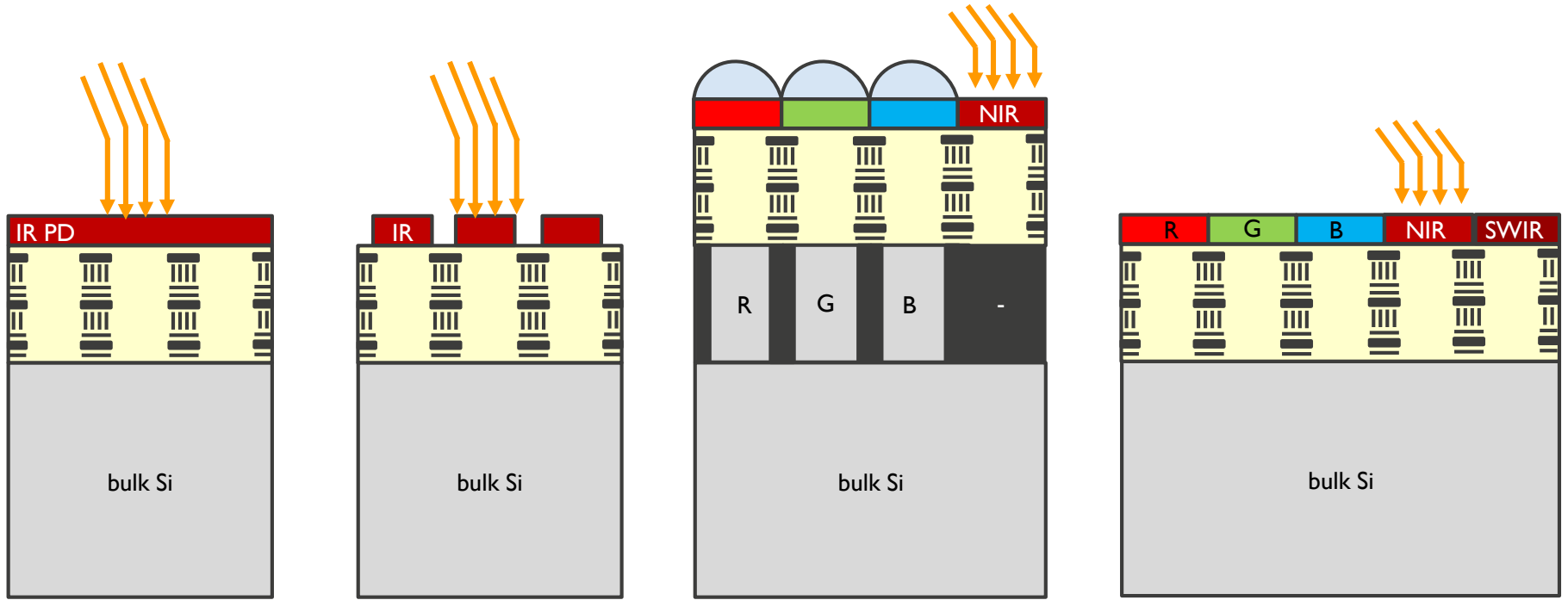
# INTEGRATION ROUTE



# INTEGRATION ROUTE



# THIN-FILM INTEGRATION OPTIONS



monolithic infrared  
*IR > 1.4  $\mu\text{m}$  (eye-safe)*

monolithic VIS-NIR  
*Si range extension (same optics)*

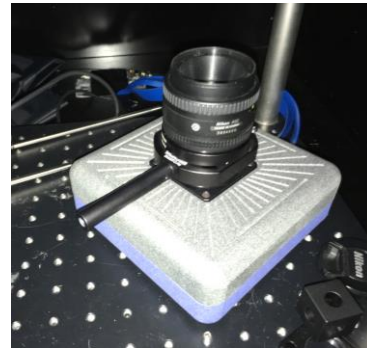
monolithic thin-film multispectral  
*several bands*

new technology

# NEXT STEPS

## FROM PIXEL STACK TO MONOLITHIC INFRARED IMAGER

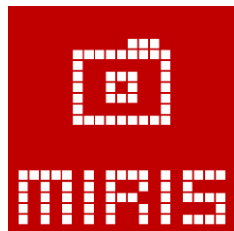
- Testing the first QD imagers
- Continuous optimization of the photodiode stack
- Further optimizing the top contact transparency
- Improving photolithographic patterning
  - High resolution pixel arrays: pitch of  $1.5\ \mu\text{m}$
  - VIS + NIR in one plane
- Scaling up to wafer level





THANK YOU!

EPIMITHEAS.GEORGITZIKIS@IMEC.BE



AGENTSCHAP  
INNOVEREN &  
ONDERNEMEN



**Vlaanderen**  
is ondernemen



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embracing a better life